

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Rule 53(b) Divisional Application of:

Kazuhiko TOKUDA

Serial No.: Division of 09/630,685

Prior Group Art Unit: 2841

Filed: August 16, 2001

Prior Examiner: A. PALADINI

For: PRINTED CIRCUIT BOARD WITH REDUCED CROSSTALK NOISE
AND METHOD OF FORMING WIRING LINES ON A BOARD TO FORM
SUCH A PRINTED CIRCUIT BOARD

INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 CFR 1.97(b)

Commissioner for Patents
Washington, D.C. 20231

August 14, 2001

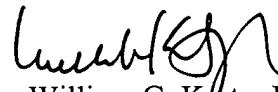
Sir:

This Information Disclosure Statement is being filed in order to comply with Applicant's duty of disclosure under 37 CFR 1.56. The documents listed on the Form PTO-1449 were made of record in parent application Serial No. 09/630,685.

The Commissioner is authorized to charge our Deposit Account No. 01-2340 for any fee which is deemed by the Patent and Trademark Office to be required to effect consideration of this statement.

Respectfully submitted,

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